

EAST Search History

Ref #	Hits	Search Query	DBs	Default Operator	Plurals	Time Stamp
S1	4	("20010016929" "6201829").PN.	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2007/05/19 17:25
S2	2	(US-20010016929-\$.did. or (US-6201829-\$.did.	US-PGPUB; USPAT	OR	ON	2007/05/19 17:25
S3	2	(US-20010016929-\$.did. or (US-6201829-\$.did.	US-PGPUB; USPAT	OR	ON	2007/05/20 15:12
S4	1	S3 and (reference near2 clock) and (transmitter) and receiver	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2007/05/20 15:07
S5	0	S4 and (test near2 clock)	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2007/05/20 15:07
S6	0	(reference near2 clock) and transmitter and (serial near2 data) and (external near2 test near2 clock) and (receiver) and (clock near2 recovery)	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2007/05/20 15:14
S7	1	(reference near2 clock) and transmitter and (serial near2 data) and (external near2 test near2 clock)	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2007/05/20 15:16
S8	1143	(reference near2 clock) and (test near2 clock)	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2007/05/20 15:16
S9	546	S8 and @pd<"20030428"	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2007/05/20 15:40

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S10	162	S9 and (transmit\$3) and receiv\$3 and (serial near2 data)	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2007/05/20 15:17
S11	69	S10 and (clock near3 recover\$3)	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2007/05/20 17:11
S12	1	S11 and ((mux or select) with (test near2 clock) with (reference near2 clock))	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2007/05/20 15:19
S13	1	(US-6208621-\$).did.	USPAT	OR	ON	2007/05/20 15:21
S14	15491	(clock near3 (comparator or comparison))	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2007/05/20 15:39
S15	684	S14 and (test near2 circuit)	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2007/05/20 15:36
S16	0	S15 and (compar\$4 with (test near2 clock) with (recovered near2 clock))	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2007/05/20 15:38
S17	2	S15 and (compar\$4 same (test near2 clock) same (recovered near2 clock))	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2007/05/20 15:38
S18	0	S14 and (compar\$4 with (test near2 clock) with (recovered near2 clock))	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2007/05/20 15:39

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S19	2	S14 and (compar\$4 same (test near2 clock) same (recovered near2 clock))	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2007/05/20 15:39
S20	4	error same (test near2 clock) same (recovered near2 clock)	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2007/05/20 15:42
S21	1	S20 and @pd<"20030428"	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2007/05/20 15:57
S22	59	(phase near2 error) and (test near2 clock) and (recovered near2 clock)	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2007/05/20 15:42
S23	30	S22 and @pd<"20030428"	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2007/05/20 15:42
S24	20	S23 and "test clock"	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2007/05/20 15:43
S25	1	(US-6208621-\$).did.	USPAT	OR	ON	2007/05/20 15:47
S26	2	(US-20010016929-\$).did. or (US-6201829-\$).did.	US-PGPUB; USPAT	OR	ON	2007/05/20 15:47
S27	2	S26 and (clock)	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2007/05/20 15:48
S28	35	("5574733" "5758104" "5787114" "5907566").PN. OR ("2001/0016929" "6201829").URPN.	US-PGPUB; USPAT; USOCR	OR	ON	2007/05/20 15:56

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S29	5	S28 and @pd<"20030428"	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2007/05/20 16:10
S30	2	("5,835,501").PN.	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2007/05/20 16:03
S31	0	("(clocknear2comparator)and(recover\$3near2clock)and("testclock")").PN.	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2007/05/20 16:09
S32	0	(clock near2 comparator) and (recover\$3 near2 clock) and ("testclock")	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2007/05/20 16:09
S33	3	(clock near2 comparator) and (recover\$3 near2 clock) and ("test clock")	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2007/05/20 16:10
S34	0	S33 and @pd<"20030428"	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2007/05/20 16:11
S35	12	(recover\$3 near2 clock) same ("test clock") same (error or compar\$4 or difference)	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2007/05/20 16:11
S36	5	S35 and @pd<"20030428"	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2007/05/20 16:24
S37	1	(US-20010016929-\$).did.	US-PGPUB	OR	ON	2007/05/20 16:18
S38	1	(US-6208621-\$).did.	USPAT	OR	ON	2007/05/20 16:19

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S39	564	"clock comparator"	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2007/05/20 16:28
S40	411	S39 and @pd<"20030428"	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2007/05/20 16:47
S41	1	S40 and (delay near2 circuit) and (latch) and ("XOR") and (counter)	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2007/05/20 16:27
S42	13	S40 and (delay near2 circuit) and (latch)	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2007/05/20 16:27
S43	37	S40 and ("clock comparator" near2 (circuit or implementation))	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2007/05/20 16:30
S44	2	S43 and (delay) and (latch) and (counter)	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2007/05/20 16:30
S45	1	(US-20020191725-\$).did.	US-PGPUB	OR	ON	2007/05/20 16:37
S46	120	(serial near2 data) and (test near2 clock) and (recovered near2 clock)	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2007/05/20 16:46
S47	77	S46 and @pd<"20030428"	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2007/05/20 16:47

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S48	64	S47 and ((clock or phase) near3 (comparator or error or difference))	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2007/05/20 16:49
S49	34	S48 and (delay and latch and counter)	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2007/05/20 16:53
S50	1	(US-20020140662-\$).did.	US-PGPUB	OR	ON	2007/05/20 16:57
S51	4233	((370/241) or (714/735) or (345/98)).CCLS.	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2007/05/20 17:04
S52	0	("L50and(clocknear2comparator)").PN.	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2007/05/20 17:04
S53	0	S51 and (clock near2 comparator)	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2007/05/20 17:04
S54	18	S51 and (clock near2 comparator)	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2007/05/20 17:04

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IEEE JNL IEEE Journal or Magazine

IET JNL IET Journal or Magazine

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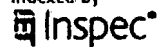
IET CNF IET Conference Proceeding

IEEE STD IEEE Standard

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IEEE JNL IEEE Journal or Magazine

IET JNL IET Journal or Magazine

IEEE CNF IEEE Conference Proceeding

IET CNF IET Conference Proceeding

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IEEE JNL IEEE Journal or Magazine

IET JNL IET Journal or Magazine

IEEE CNF IEEE Conference Proceeding

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1. Decision and clock recovery circuits for gigahertz optical fiber receivers

Enam, S.; Abidi, A.;

[Lightwave Technology, Journal of](#)

Volume 5, Issue 3, Mar 1987 Page(s):367 - 372

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2. Investigations and measurements of the dynamic performance of high speed

Hagelauer, R.; Oehler, F.; Rohmer, G.; Sauerer, J.; Seitzer, D.; Schmitt, R.; Wi

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3. Analogue to digital convertor architecture with a signal level detector

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5. 3 GHz resonant tunneling clocked comparator

Brar, B.; Broekaert, T.P.E.; van der Wagt, J.P.A.; Seabaugh, A.C.; Moise, T.S.

Beam, E.A., III.; Frazier, G.A.;





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4-6 Aug. 1997 Page(s):28 - 34

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-  **6. A high speed offset-compensated differential comparator in floating body technology for radiation hard switched-capacitor systems**
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6-9 Oct. 1997 Page(s):160 - 161
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-  **7. Static and dynamic on-chip test response evaluation using a two-mode c**
De Venuto, D.; Ohletz, M.J.; Matarrese, G.;
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-  **8. A synchronous dual-output switching dc-dc converter using multibit noise control**
May, M.W.; May, M.R.; Willis, J.E.;
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5-7 Feb. 2001 Page(s):358 - 359
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-  **9. Superconducting bandpass $\Delta\Sigma$ modulator with 2.23 GHz center frequency and sampling rate**
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[Solid-State Circuits Conference, 2002. Digest of Technical Papers. ISSCC, 2002](#)
Volume 1, 3-7 Feb. 2002 Page(s):188 - 459 vol.1
Digital Object Identifier 10.1109/ISSCC.2002.992999
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 **PALM INTRANET****Inventor Name Search Result**

Your Search was:

Last Name = CANDELA

First Name = DAN

Application#	Patent#	Status	Date Filed	Title	Inventor Name
10825847	Not Issued	30	04/16/2004	Domain of definition in warper/morpher	CANDELA, DAN
11271081	Not Issued	30	11/11/2005	Locking relationships among parameters in computer programs	CANDELA, DAN
11271162	Not Issued	30	11/11/2005	Visually encoding nodes representing stages in a multi-stage video compositing operation	CANDELA, DAN
11408741	Not Issued	30	04/21/2006	3D histogram and other user interface elements for color correcting images	CANDELA, DAN
11408783	Not Issued	25	04/21/2006	3D lut techniques for color correcting images	CANDELA, DAN
11409553	Not Issued	25	04/21/2006	Workflows for color correcting images	CANDELA, DAN

Inventor Search Completed: No Records to Display.

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Time: 09:28:59 **PALM INTRANET****Inventor Name Search Result**

Your Search was:

Last Name = MIDDLEL

First Name = MITCH

Application#	Patent#	Status	Date Filed	Title	Inventor Name
10825847	Not Issued	30	04/16/2004	Domain of definition in warper/morpher	MIDDLEL, MITCH
11107328	Not Issued	93	04/16/2005	DEPTH ORDERING OF PLANES AND DISPLAYING INTERCONNECTS HAVING AN APPEARANCE INDICATING DATA CHARACTERISTICS	MIDDLEL, MITCHELL SCOTT
11107329	Not Issued	30	04/16/2005	Processing cursor movements in a graphical user interface of a multimedia application	MIDDLEL, MITCHELL SCOTT

Inventor Search Completed: No Records to Display.

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 **PALM INTRANET****Inventor Name Search Result**

Your Search was:

Last Name = BRINKMANN

First Name = RON

Application#	Patent#	Status	Date Filed	Title	Inventor Name
10825847	Not Issued	30	04/16/2004	Domain of definition in warper/morpher	BRINKMANN, RON
11107328	Not Issued	93	04/16/2005	DEPTH ORDERING OF PLANES AND DISPLAYING INTERCONNECTS HAVING AN APPEARANCE INDICATING DATA CHARACTERISTICS	BRINKMANN, RON
11271081	Not Issued	30	11/11/2005	Locking relationships among parameters in computer programs	BRINKMANN, RON
11271162	Not Issued	30	11/11/2005	Visually encoding nodes representing stages in a multi-stage video compositing operation	BRINKMANN, RON
09713659	7059802	150	11/15/2000	VIBRATORY COMPACTOR AND COMPACT EXCITER ASSEMBLY USABLE THEREWITH	BRINKMANN, RONALD
10615074	Not Issued	168	07/08/2003	Vibratory compactor and compact exciter assembly usable therewith	BRINKMANN, RONALD
29134073	D454141	150	12/13/2000	Vibratory trench roller	BRINKMANN, RONALD
29143376	D461197	150	06/12/2001	VIBRATORY TRENCH ROLLER	BRINKMANN, RONALD
29074410	D396727	150	08/01/1997	RIDING ROLLER	BRINKMANN, RONALD W.

Inventor Search Completed: No Records to Display.

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 **PALM INTRANET****Inventor Name Search Result**

Your Search was:

Last Name = WOLBERG

First Name = GEORGE

Application#	Patent#	Status	Date Filed	Title	Inventor Name
07387605	5204944	150	07/28/1989	SEPARABLE IMAGE WARPING METHODS AND SYSTEMS USING SPATIAL LOOKUP TABLES	WOLBERG, GEORGE
08343390	5537226	150	11/22/1994	METHOD FOR RESTORING IMAGES SCANNED IN THE PRESENCE OF VIBRATION	WOLBERG, GEORGE
10732855	6935058	150	12/09/2003	AUTOMATIC PAGE TURNER WITH BELT DRIVE ELEMENT	WOLBERG, GEORGE
10747989	7019203	150	12/29/2003	AUTOMATIC PAGE TURNER WITH TURNSTILE ELEMENT	WOLBERG, GEORGE
10825847	Not Issued	30	04/16/2004	Domain of definition in warper/morpher	WOLBERG, GEORGE
11423406	Not Issued	30	06/09/2006	Electronic Page Turner with Improved Pickup Mechanism	WOLBERG, GEORGE

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